

BOARD STACK UP
THIS IS A 2 SIDED 32 MIL PCB

TOP SILK SCREEN
TOP SOLDER MASK
TOP METAL 1 OZ COPPER
30 MIL DIELECTRIC (APROX)
BOTTOM METAL LAYER 1 OZ COPPER
BOTTOM SOLDER MASK
BOTTOM SILK SCREEN
OVERALL BOARD THICKNESS IS APPROX. 32 MILS

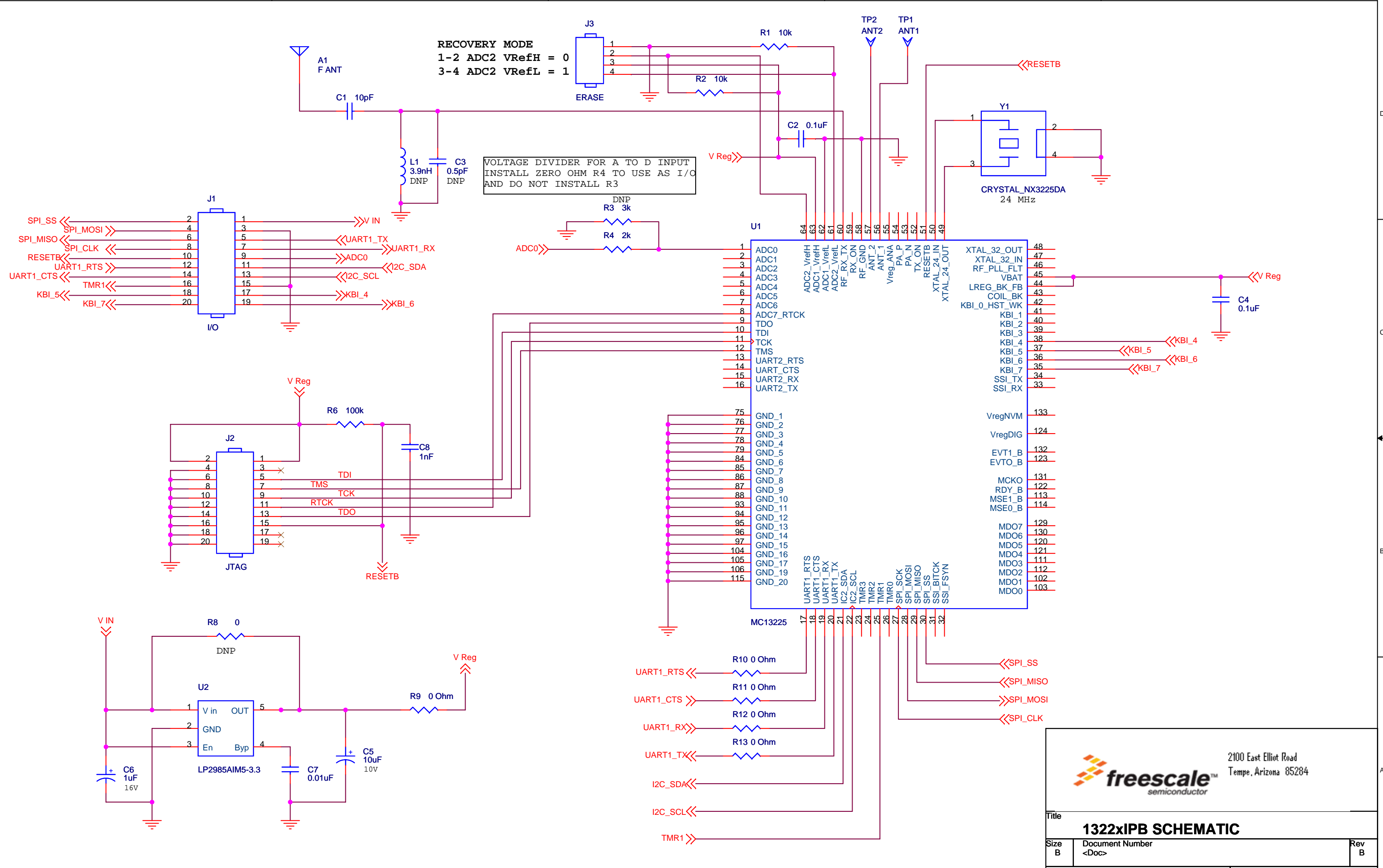
NOTES

CONNECTOR ORIENTATION
J1 I/O CONNECTOR, IS MOUNTED ON THE BACK SIDE OF THE PCB
THIS CAN BE A STRAIGHT 2X10 HEADER OR A 2X10 RIGHT ANGLE
HEADER. TO MAINTAIN CORRECT PIN ORIENTATION IT MUST BE
ON THE BACK OF THE PCB
J2, JTAG AND J3, ERASE CONNECTORS ARE MOUNTED ON THE TOP
SIDE OF THE PCB

FLASH ERASE
INSTALL SHUNTS FROM J3 1to2 AND 3to4
POWER UP THE PCB FOR AT LEAST 2 SECONDS TO ERASE FLASH



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RECOVERY MODE
 1-2 ADC2 VrefH = 0
 3-4 ADC2 VrefL = 1

VOLTAGE DIVIDER FOR A TO D INPUT
 INSTALL ZERO OHM R4 TO USE AS I/O
 AND DO NOT INSTALL R3

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 semiconductor

Title: **1322xIPB SCHEMATIC**

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